

ATC CABINET PHASE ASSIGNMENTS

General Overview

The following document is designed to help ATC cabinet users identify the default phase assignments for a 16-channel output assembly.

This document outlines the following:

- Phase, overlap, and pedestrian assignments
- CMU channel assignments
- SIU output (IO) assignments
- HDSP addresses

This document also includes:

- SIU input (IO) assignments for 24-channel input assembly #1
- SIU input (IO) assignments for 24-channel input assembly #2

This page was intentionally left blank

Phase/Overlap/Pedestrian Assignments

CH1/2	CH3/4	CH5/6	CH7/8	CH9/10	CH11/12	CH13/14	CH15/16	SUI 1
HDSP1	HDSP2	HDSP3	HDSP4	HDSP5	HDSP6	HDSP7	HDSP8	SIU 1
Ph 1	Ph 3	Ph 5	Ph 7	OLA	OLC	Ped 2	Ped 6	
Ph 1	Ph 3	Ph 5	Ph 7	OLA	OLC			
Ph 1	Ph 3	Ph 5	Ph 7	OLA	OLC	Ped 2	Ped 6	
Ph 2	Ph 4	Ph 6	Ph 8	OLB	OLD	Ped 4	Ped 8	
Ph 2	Ph 4	Ph 6	Ph 8	OLB	OLD			
Ph 2	Ph 4	Ph 6	Ph 8	OLB	OLD	Ped 4	Ped 8	

CMU Channel (Ch) Assignments

CH1/2	CH3/4	CH5/6	CH7/8	CH9/10	CH11/12	CH13/14	CH15/16	SUI 1
HDSP1	HDSP2	HDSP3	HDSP4	HDSP5	HDSP6	HDSP7	HDSP8	SIU 1
Ch-1	Ch-3	Ch-5	Ch-7	Ch-9	Ch-11	Ch-13	Ch-15	
Ch-1	Ch-3	Ch-5	Ch-7	Ch-9	Ch-11	Ch-13	Ch-15	
Ch-1	Ch-3	Ch-5	Ch-7	Ch-9	Ch-11	Ch-13	Ch-15	
Ch-2	Ch-4	Ch-6	Ch-8	Ch-10	Ch-12	Ch-14	Ch-16	
Ch-2	Ch-4	Ch-6	Ch-8	Ch-10	Ch-12	Ch-14	Ch-16	
Ch-2	Ch-4	Ch-6	Ch-8	Ch-10	Ch-12	Ch-14	Ch-16	

SIU Output (IO) Assignments (CU SIU Address = 1)

CH1/2	CH3/4	CH5/6	CH7/8	CH9/10	CH11/12	CH13/14	CH15/16	SUI 1
HDSP1	HDSP2	HDSP3	HDSP4	HDSP5	HDSP6	HDSP7	HDSP8	SIU 1
IO 0	IO 6	IO 12	IO 18	IO 24	IO 30	IO 36	IO 42	
IO 1	IO 7	IO 13	IO 19	IO 25	IO 31	IO 37	IO 43	
IO 2	IO 8	IO 14	IO 20	IO 26	IO 32	IO 38	IO 44	
IO 3	IO 9	IO 15	IO 21	IO 27	IO 33	IO 39	IO 47	
IO 4	IO 10	IO 16	IO 22	IO 28	IO 34	IO 40	IO 48	
IO 5	IO 11	IO 17	IO 23	IO 29	IO 35	IO 41	IO 49	

HDSP Addresses

CH1/2	CH3/4	CH5/6	CH7/8	CH9/10	CH11/12	CH13/14	CH15/16	SUI 1
HDSP1	HDSP2	HDSP3	HDSP4	HDSP5	HDSP6	HDSP7	HDSP8	SIU 1
0 (00000)	1 (00001)	2 (00010)	3 (00011)	4 (00100)	5 (000101)	6 (00110)	7 (00111)	

ATC Cabinet 24-Channel Input Assembly #1

SIU Input (IO) Assignments (CU SIU Address = 9)

2-Ch Card	2-Ch Card	2-Ch Card	2-Ch Card	2-Ch Card	2-Ch Card	2-Ch Card	2-Ch Card	2-Ch Card	2-Ch Card	2-Ch Card	2-Ch Card	2-Ch Card	SIU 9	
IO 6	IO 8	IO 10	IO 12	IO 14	IO 16	IO 18	IO 20	IO 22	IO 24	IO 26	IO 28	IO 28		
IO 7	IO 9	IO 11	IO 13	IO 15	IO 17	IO 19	IO 21	IO 23	IO 25	IO 27	IO 29	IO 29		
	*IO 6	*IO 8	*IO 10	*IO 12	*IO 14	*IO 16	*IO 18	*IO 20	*IO 22	*IO 24	*IO 26	*IO 26		
	*IO 7	*IO 9	*IO 11	*IO 13	*IO 15	*IO 17	*IO 19	*IO 21	*IO 23	*IO 25	*IO 27	*IO 27		

** If 4-channel device is being used.*

ATC Cabinet 24-Channel Input Assembly #2

SIU Input (IO) Assignments (CU SIU Address = 10)

2-Ch Card	2-Ch Card	2-Ch Card	2-Ch Card	2-Ch Card	2-Ch Card	2-Ch Card	2-Ch Card	2-Ch Card	2-Ch Card	2-Ch Card	2-Ch Card	2-Ch Card	SIU 10	
IO 6	IO 8	IO 10	IO 12	IO 14	IO 16	IO 18	IO 20	IO 22	IO 24	IO 26	IO 28	IO 28		
IO 7	IO 9	IO 11	IO 13	IO 15	IO 17	IO 19	IO 21	IO 23	IO 25	IO 27	IO 29	IO 29		
	*IO 6	*IO 8	*IO 10	*IO 12	*IO 14	*IO 16	*IO 18	*IO 20	*IO 22	*IO 24	*IO 26	*IO 26		
	*IO 7	*IO 9	*IO 11	*IO 13	*IO 15	*IO 17	*IO 19	*IO 21	*IO 23	*IO 25	*IO 27	*IO 27		